

WHAT THE INVENTION CLAIMED IS

1. A write-protected micro memory device, comprising:

a single chip flash memory controller, having a write-protect parameter;

at least one flash memory divided into one or multiple blocks, connecting to said

5 single chip flash memory controller, wherein said write-protect parameter is marked to write-protect a block of said flash memory; and

an interface circuit, connecting to said single chip flash memory controller, and a host connected between said memory device and said host;

wherein when said host retrieves/stores data or program from/to said flash
10 memory, said single chip flash memory controller can prevent said host to write data into said marked block in the flash memory according to said preset write-protect parameter.

2. The write-protected micro memory device according to claim 1, wherein said single chip flash memory controller is a programmable firmware, which is inlaid in or external to a mask ROM, wherein a program or data recorded once in said mask ROM
15 to enable the user to set said write-protect parameter, and said parameter has a write-protection to said marked block.

3. The write-protected micro memory device according to claim 1, wherein said single chip flash memory controller is a programmable firmware, which is inlaid in or external to a rewriteable memory, wherein a program or data recorded once in said
20 rewriteable memory to enable the user to set said write-protect parameter, and said parameter has a write-protection to said marked block.

4. The write-protected micro memory device according to claim 3, wherein said rewriteable memory is comprised of EEPROM or Nor Type Flash.

5. The write-protected micro memory device according to claim 1, wherein said interface circuit is comprised of an USB interface circuit, PCMCIA or ATA IDE interface.

6. The write-protected micro memory device according to claim 1, wherein
5 said single chip flash memory controller is to convert a control signal of said host into a signal for controlling said flash memory, and to manage and program properly for said flash memory to make said single chip flash memory controller receiving a command from said host (including writing address and data), and send out said writing command to said flash memory then write said address and data transmitted from said host
10 orderly into said flash memory.

7. The write-protected micro memory device according to claim 1, wherein said memory device is comprised of a memory card.

8. The write-protected micro memory device according to claim 1, wherein said memory device is comprised of a memory stick.

15 9. The write-protected micro memory device according to claim 1, wherein said host is comprised of a computer.

10. The write protected micro memory device according to claim 1, wherein said host is comprised of a card reader.

11. A write-protected micro memory device, comprising:
20 an interface circuit, connecting to a host;
at least one flash memory, divided into one or multiple blocks, comprising a redundant block having a write-protect parameter; and

a single chip flash memory controller, connecting to said interface circuit and said flash memory respectively;

wherein when said host retrieves/stores data or program from/to said flash memory, said single chip flash memory controller can prevent said host to write data into
5 said marked block in the flash memory according to said preset write-protect parameter.

12. The write-protected micro memory device according to claim 11, wherein said flash memory is comprised of a 32 MB NOR Type Flash, said 32 MB NOR Type Flash has 2048 blocks, and each block contains 32 pages; a page is a smallest data transmission unit, and said page is consisted of a 512 byte data area and a 16 byte
10 redundant area.

13. The write-protected micro memory device according to claim 11, wherein said interface circuit is comprised of an USB interface circuit, PCMCIA or ATA IDE interface.

14. The write-protected micro memory device according to claim 11, wherein
15 said memory device is comprised of a memory card.

15. The write-protected micro memory device according to claim 11, wherein said memory device comprised of a memory stick.

16. The write-protected micro memory device according to claim 11, wherein said host is comprised of a computer.

20 17. The write protected micro memory device according to claim 11, wherein said host is comprised of a card reader.